**MCQ on COMPUTER ORGANIZATION AND ARCHITECTURE**

 1. Which of the following operations is/are performed by the ALU?

1. Data manipulation
2. Exponential
3. Square root
4. All of the above

**Answer:** (d)

**2.** Which of the following format is used to store data?

1. Decimal
2. Octal
3. BCD
4. Hexadecimal

**Answer:** (c)

3. Which of the following memory of the computer is used to speed up the computer processing?

1. Cache memory
2. RAM
3. ROM
4. None of the above

**Answer:** (a)

4. The address in the main memory is known as -

1. Logical address
2. Physical address
3. Memory address
4. None of the above

**Answer:** (b)

5. Which of the following memory unit communicates directly with the CPU?

1. Auxiliary memory
2. Main memory
3. Secondary memory
4. None of the above

**Answer:** (b)

6. Where is the decoded instruction stored?

1. Registers
2. MDR
3. PC
4. IR

**Answer:** (d)

7. The Program Counter is also called as -

1. Instruction Pointer
2. Data Counter
3. Memory pointer
4. None of the above

**Answer:** (a)

8. What does a computer bus line consists of?

1. Set of parallel lines
2. Accumulators
3. Registers
4. None of the above

**Answer:** (a)

9.  Which of the architecture is power efficient?  
a) RISC  
b) ISA  
c) IANA  
d) CISC

**Answer:** (a)

10. Floating point representation is used to store

a) Boolean values

b) Whole numbers

c) Real integers

d) Integers

**Answer:** (c)

11. The queue is also known as

a. Flash memory

b. FILO memory

c. Flash memory

d. FIFO memory

Answer: d

12. The basic building blocks of the arithmetic logic unit in digital computers are known as

a. Adders

b. Attenuator

c. Demultiplexer

d. Subtractors

Answer: a

13. In the Principle of locality, there is a justification of the use of:

a. DMA

b. Cache memory

c. Threads

d. Interrupts

Answer: (b)

14. Which of these is NOT involved in the case of a memory write operation?

a. Databus

b. MDR

c. MAR

d. PC

Answer: (d)

15. Which of these is required when we want to establish the communication links between a CPU and its peripherals?

a. Memory data register

b. Memory address register

c. Instruction register

d. Index register

Answer: (a)

16. Which of these memories would have the lowest access time in a system:

a. Main Memory

b. Magnetic Disk

c. Registers

d. Cache

Answer: (c)

17. Which of these addressing modes is the most suitable for some high-level language statements?

a. Indexed

b. Auto-decrement

c. Auto-increment

d. Displacement

Answer: (b)

18. What is the hexadecimal representation of 6578?

a. D71

b. 32F

c. 1AF

d. D78

Answer: (c)

19. Any instruction initiates interrupt which are:

a. hardware

b. software

c. internal

d. external

Answer: b

20.What do processors of all computers must have?

a. Control unit

b. ALU

c. Primary Storage

d. All of these

Answer: d

21.In the CPU, what is the functionality of the control unit?

a. To decode program instruction

b. To perform logic operations

c. To store program instruction

d. To transfer data to primary storage

Answer: a

22. What is used to increase the apparent size of physical memory?

a. Disks

b. Hard-disk

c. Virtual memory

d. Secondary memory

Answer: c

23. The most common addressing techniques employed by a CPU is:

a. Direct

b. Indirect

c. Immediate

d. All of these

Answer: d

24. What does the control unit generate to control other units?

a. Transfer signals

b. Command Signals

c. Control signals

d. Timing signals

Answer: d

25. What are encoded characters & numbers used as operands?

a. Stored Values

b. Information

c. Data

d. Input

Answer: c